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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/641,706	08/15/2003	Eitan Rosen	MP0287	9969
26200	7590	03/09/2006	EXAMINER	
FISH & RICHARDSON P.C. P.O BOX 1022 MINNEAPOLIS, MN 55440-1022			BUTLER, DENNIS	
			ART UNIT	PAPER NUMBER
			2115	
DATE MAILED: 03/09/2006				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/641,706

Applicant(s)

ROSEN, EITAN

Examiner

Dennis M. Butler

Art Unit

2115

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 August 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-39 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4, 8, 13-15, 22-29, 33, 38 and 39 is/are rejected.
- 7) ☒ Claim(s) 5-7, 9-12, 16-21, 30-32 and 34-37 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 15 August 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 08/15/03.
- ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Other: _____.

Art Unit: 2115

1. This action is in response to the application filed on August 15, 2003. Claims 1-39 are pending.

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1-4, 8, 13-15, 22-29, 33 and 38-39 are rejected under 35 U.S.C. 102(e) as being anticipated by Lin, Published U. S. Patent Application 2002/0157031.

Per claims 1 and 26:

A) Lin teaches the following claimed items:

1. a first delay circuit (delay 160) and a second delay circuit (delay 140 and delay 142) responsive to a clock signal (CLKIN) and in a parallel configuration with figures 2 and 5 and at paragraphs 26 and 43;
2. control circuitry responsive to the output of the first delay circuit and the clock signal to adjust the amount of delay of the first delay circuit and further responsive to the output of the second delay circuit to adjust the amount of delay of the second delay circuit with controller 162 and controller 146 of figures 2 and 5 and at paragraphs 29-30 and 44.

Per claims 2-4, 8, 13-14, 27-29, 33 and 38-39:

Lin describes data sampling circuitry responsive to the sampling output of the first delay and responsive to the sampling output of the second delay with data latch 130, Din 126 and CAPCLK 118 of figure 2, with figure 3 and at paragraphs 24-25 and 39. Lin describes a comparator responsive to the first delay with phase detector 180 of figure 2 and a comparator responsive to the second delay with phase detector 148 of figure 5. Lin describes synchronizing according to a double data rate protocol at paragraphs 50 and 53.

Per claim 15:

A) Lin teaches the following claimed items:

1. receiving a clock signal (XCLK) synchronized with a data signal (DQ) with figures 2 and 3 and at paragraphs 25 and 34;
2. generating a first sampling signal (CAPCLK) that is delayed by a first delay (delay 160) with respect to the clock signal with figure 2 and at paragraphs 24-25 and 28-30;
3. generating a second sampling signal (CAPCLK) that is delayed by a second different delay (delay 140, delay 142 and delay 160) with respect to the clock signal with figures 2-3 and 5 and at paragraphs 24-25 and 43-44;
4. sampling the data signal based on the first sampling signal with figure 2 and at paragraphs 24-25;
5. sampling the data signal based on the second sampling signal with figures 2-3 and at paragraphs 24-25 and 34-36.

Per claims 22-25:

Lin describes sampling the data signal at the midpoint with figure 3 signals XCLK, CAPCLK and Din) and at paragraph 8 and the last three lines of paragraph 34.

4. Claims 5-7, 9-12, 16-21, 30-32 and 34-37 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dennis M. Butler whose telephone number is 571-272-3663. The fax number for this unit is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Dennis M. Butler
Dennis M. Butler
Primary Examiner
Art Unit 2115